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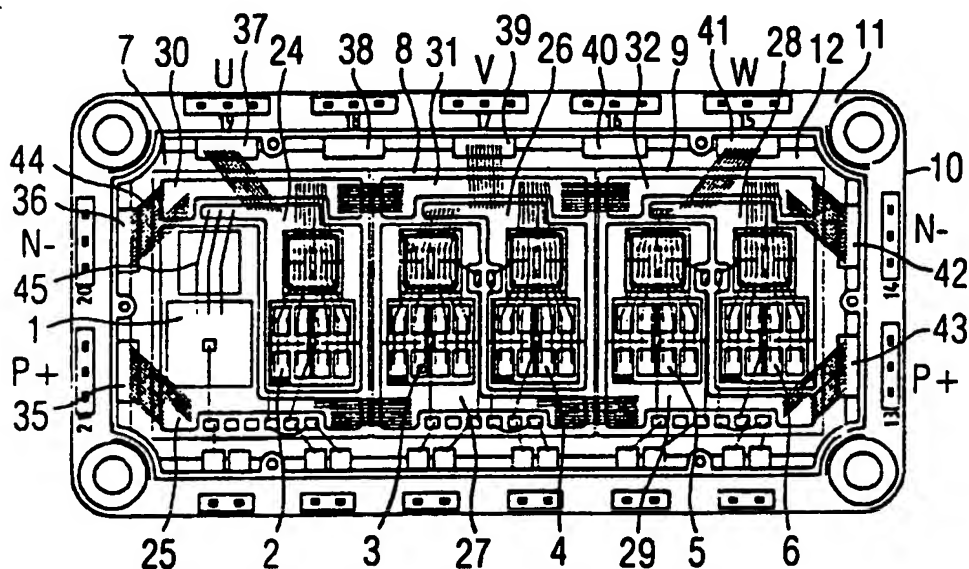
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(54) **Leistungs-Halbleitermodul**

(57) Das Modul enthält sechs steuerbare Leistungs-Halbleiterbauelemente (1 bis 6), die auf Leiterbahnen (25 bis 29) tragenden isolierenden Substraten angeordnet sind. Für je zwei nebeneinander liegende Halbleiterbauelemente ist auf dem Substrat eine zusätzliche Leiterbahn (37, 38, 39) vorgesehen, die mit einem von

sechs Laststromanschlüssen (13 bis 21), mit einer der Leiterbahnen und mit einem der Halbleiterbauelemente verbindbar ist. Damit können ohne konstruktive Änderungen mehrere Schaltungsvarianten im Modul verwirklicht werden.

FIG 1



Title: Power semiconductor module
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Inventor: Dr.-Ing. Andreas Lenninger

Abstract:

The module contains six controllable power semiconductor components (1 to 6) which are arranged on insulating substrates carrying conductive strips (25 to 29). For every two adjacent semiconductor components, an additional conductive strip (37, 38, 39) is provided on the substrate and can be connected with one of the conductive strips and with one of the semiconductor components. Thus several circuit variations can be embodied in the module without having to change the design.

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Translator's Declaration issued under authority of the
Association of Translators and Interpreters of Ontario Act, L.C. 1989.**Certificate of Accuracy of Translation**

I, the undersigned,


PETER HESSEL, C.Tran. (Canada)

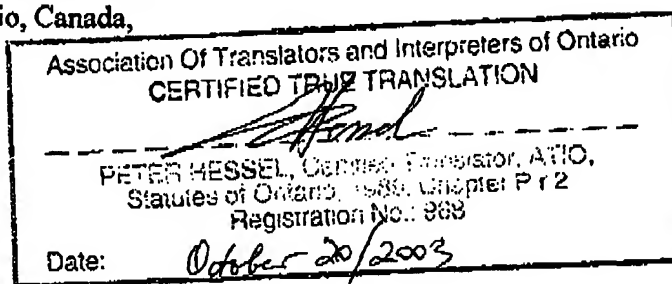
hereby certify

- (1) that I am fully conversant with both the English and the German languages;
- (2) that I have translated into English:
Abstract only of Filing No.: 96109428.1
- (3) that the translation is to the best of my knowledge and belief an accurate translation from the original into the English language.

I, the undersigned declare further that all statements made herein of my own knowledge are true and that all statements made of information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the matter with which this translation is used.

DECLARED at the Township of McNab/Braeside,
County of Renfrew, Province of Ontario, Canada,
this 20th day of October, 2003


Peter Hessel,
Certified Translator, ATIO



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